

FIG. 1

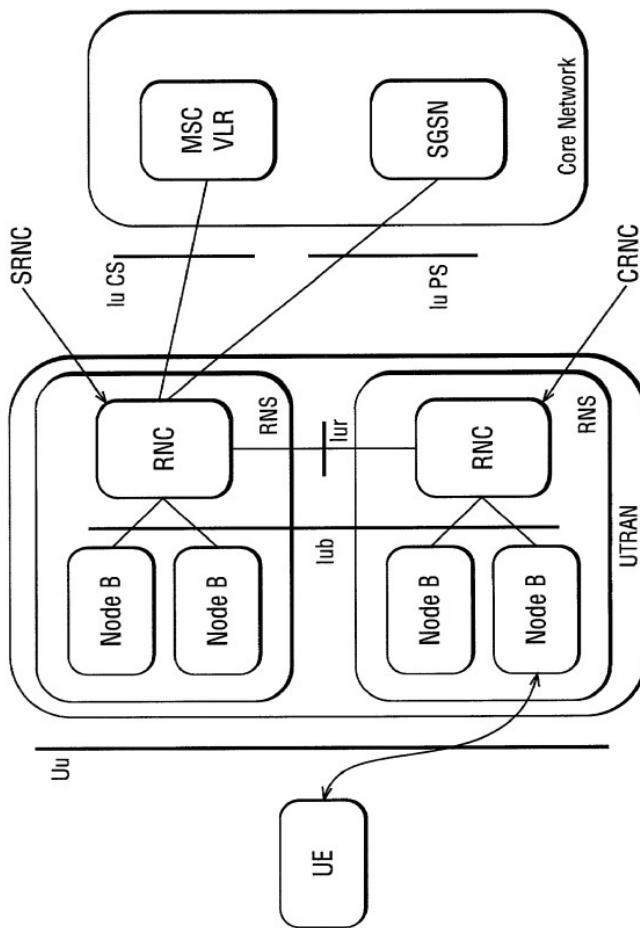


FIG. 2

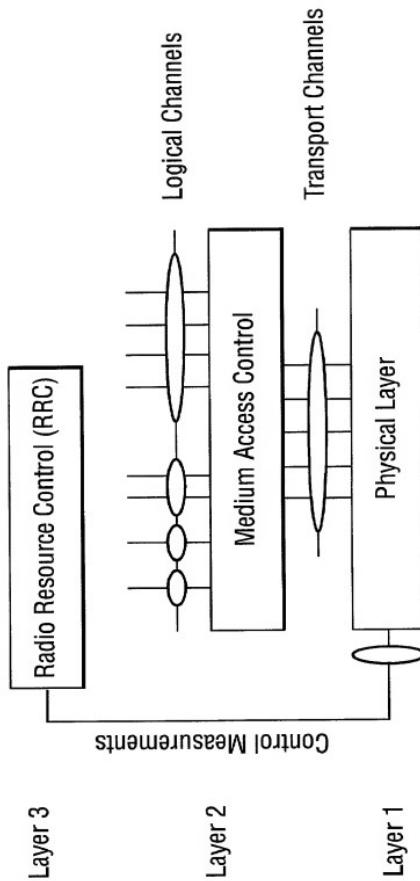


FIG. 3 GP denotes the guard period and CP the chip periods

Chip number (CN)	Length of field in chips	Contents of field
0-975	976	Data symbols
976-1487	512	Midamble
1488-2463	976	Data symbols
2464-2559	96	Guard period

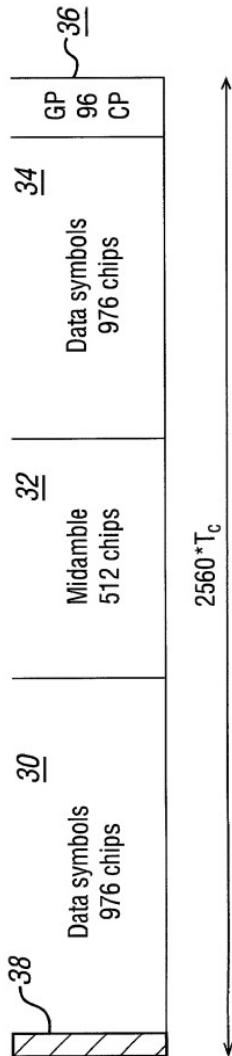


FIG. 4

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Definition	'RX Timing Deviation' is the time difference $\text{TRX}_{\text{dev}} = \text{TTS} - \text{TRX}_{\text{path}}$ in chips, with TRX_{path} : time of the reception in the Node B of the first significant uplink path to be used in the detection process
TTS:	time of the beginning of the respective slot according to the Node B internal timing

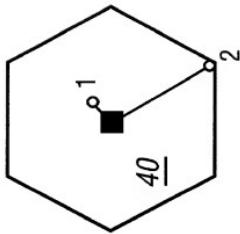
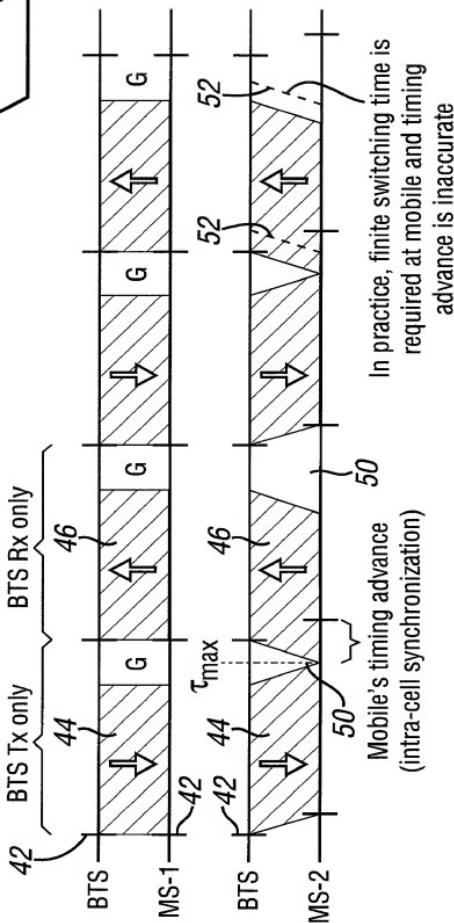


FIG. 5

Maximum cell radius is limited by the guard period.

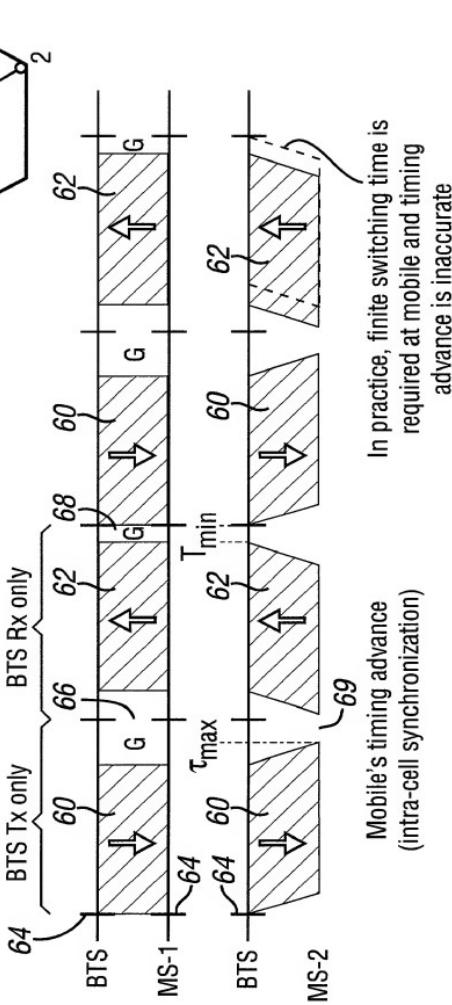


$$T_{guard} = 2\tau_{max}$$

$$T_{guard} = 25 \mu s \Rightarrow \tau_{max} = 12.5 \mu s \Rightarrow r_{max} = 3750 m$$

FIG. 6

Maximum cell radius is limited by the guard period.



$$T_{\text{Quad}} = 1/2T_{\text{max}} \quad T_{\text{Quad}} = 25 \mu\text{s} \iff T_{\text{max}} = 50 \mu\text{s} \iff r_{\text{max}} = 7500 \text{ m}$$

FIG. 7

